



PATENT
Customer No. 22,852
Attorney Docket No. 03180.0326

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Tadashi IGUCHI et al.)	Group Art Unit: 2826
)	
Application No.: 10/612,033)	Examiner: Quinto, Kevin V.
)	
Filed: July 3, 2003)	
)	
For: SEMICONDUCTOR MEMORY)	
PREVENTING AN ELECTRIC)	
SHORT CIRCUIT BETWEEN A)	
WORD LINE AND A)	
SEMICONDUCTOR SUBSTRATE,)	
AND MANUFACTURING METHOD)	
FOR THE SEMICONDUCTOR)	
MEMORY (As Amended))	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(c)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), Applicants bring to the attention of the Examiner the document listed on the attached Form PTO/SB/08. This Information Disclosure Statement (IDS) is being filed after the events recited in Section 1.97(b) but, to the undersigned's knowledge, before the mailing date of either a Final action, Quayle action, or a Notice of Allowance. An Amendment in response to the non-final Office Action dated February 9, 2005, is filed concurrently herewith.

The document listed in this IDS was first cited in a Notification of Reasons for Refusal mailed on February 8, 2005, issued by the Japanese Patent Office in a

counterpart foreign application. This IDS is filed within three months of the mailing date of the Notification.

In lieu of a statement of relevance or translation of the non-English document, the Notification of Reasons of Refusal from the Japanese Patent Office which cited that document and set forth the relevance thereof and an English language translation of that Notification are enclosed.

Applicants respectfully request that the Examiner consider the listed document and indicate that it was considered by making appropriate notations on the attached form.

Applicants note that the Notification also discloses the relevance of Japanese Laid-Open Application No. 8-125148 previously submitted in an IDS filed July 3, 2003.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitute "prior art." If the Examiner applies the document as prior art against any claims in the application and Applicants determine that the cited document does not constitute "prior art" under United States law, Applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such document.


Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please
charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: May 5, 2005

By: 
Qingyu Yin*

*With limited recognition under 37 C.F.R. § 10.9(b)